

**Company Contact**

System Management Interface Forum  
(646) 714-3362  
[admin@smiforum.org](mailto:admin@smiforum.org)

**Revised PMBus™ Power System Management Protocol Specification  
V1.3 to be Introduced at APEC 2014**

- *PMBus Specification V1.3 increases data throughput and broadens relevance to market applications employing adaptive voltage scaling*
- *Follow-up Q&A session to occur at adjacent Omni Hotel*

**Richardson, TX, February 4, 2014** — On Tuesday, March 18, 2014 between 8:30AM and 12 noon, during the Applied Power Electronics Conference and Exposition (APEC) held at the Fort Worth Convention Center, the revised PMBus Power System Management Protocol Specification V1.3 will be introduced. Power Electronics Standards session IS2-1-5, “An Introduction to Revision 1.3 of the PMBus Specification” will be presented by Travis Summerlin from Texas Instruments and Mike Jones from Linear Technology. At 4PM that afternoon, a one hour follow-up session entitled “Live Q&A: Find Out More About Revision 1.3 of the PMBus Specification” will be held at the adjacent Omni Hotel in Fort Worth Ballroom #5.

The PMBus Power System Management Protocol Specification provides a standardized method of communication to power conversion and power management devices. A set of power supply oriented commands provides a common method for configuring, controlling and monitoring power. The PMBus protocol is transported over the SMBus serial bus due to its cost effectiveness and common use in the market. I2C compatible designs of the SMBus allow PMBus use in a number of markets that are familiar with and that already implement this form of serial bus. System designs using SMBus or I2C use a host controller to ‘master’ the bus.



PMBus V1.3 will be backward compatible. The significant differences between PMBus V1.3 and the previous PMBus V1.2 are summarized as follows:

1. 1 MHz bus speed vs. 400 kHz limit for PMBus V1.2.
2. Floating point data format: supports NaN and +/-Inf, 16 bit number, IEEE 754 half precision and it allows easy conversion to C types.
3. Relative voltage thresholds: allows programming all output voltage related values thresholds as a percentage of the output voltage.
4. Zone Read/Write: enables partitioning of devices by zone for intelligent queries and operations.
5. Adaptive voltage scaling (AVS): dedicated bus to statically and dynamically control processor voltages.
6. First to Fault mechanism: enables easier detection of the root cause of a fault.

The PMBus standard has quickly been adopted in major market segments due to its flexibility and robustness. With its embellishments, V1.3 has broad implications for manufacturers of FPGAs, ASICs, SoCs, networking and communication processors, core processors for computing, server and storage markets, and any other type of core processor that will benefit by adaptively altering its own supply voltage.

By adopting the PMBus standards, companies are more inclined to develop competitive products with broader market appeal. For more information, visit <http://pmbus.org/index.php> and <http://www.apec-conf.org/conference/industry-sessions/>.

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**Photography**

	Caption: <b>PMBus Power System Management Protocol Specification V1.3 to be Introduced at APEC 2014</b>
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**Contact Notes**

**SMIF/PMBus**

System Management Interface Forum  
501 W President George Bush Hwy STE 130  
Richardson, TX 75080  
USA  
Tel: 646 714-3362  
[admin@smiforum.org](mailto:admin@smiforum.org)

**PR Representative**

Ira Sabran  
Lodico & Co.  
60 McAllister Drive  
Carlisle, MA 01741  
USA  
Tel: 978 369 6556 x10  
[isabran@lodicoandco.com](mailto:isabran@lodicoandco.com)

**About The System Management Interface Forum (SMIF)**

The System Management Interface Forum (SMIF), Inc., supports the rapid advancement of an efficient and compatible technology base that promotes power management and systems technology implementations. The group's activities include: promoting global development of communications protocols; identification of appropriate applications; providing global educational services; promoting worldwide compatibility and interoperability; and identifying, selecting, augmenting as appropriate, and publishing specifications. The SMIF provides a membership path for any company or individual to be active participants in any or all of the various working groups established by the implementers forums.