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APEC 2015, Charlotte, North Carolina, March 15 - 19

SMIF to host presentation of 'real world' applications papers at APEC 2015 to guide potential and existing adopters of the PMBus specification

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Expert PMBus adopters from leading semiconductor and electronics industry companies to present wide-ranging papers on PMBus protocol which underpins the optimized digital management of power supply products

Richardson, TX, February 2, 2015 — The System Management Interface Forum (SMIF Inc.) in conjunction with representatives from several of its member companies, will be presenting a number of papers centered around Power Management Bus (PMBus™) standards and applications at this year's Applied Power Electronics Conference and Exposition (APEC). The event, which is an important gathering for the global power electronics industry, will take place in Charlotte, North Carolina from March 15th to 19th. The SMIF will also use this year's APEC as the forum at which to launch PMBus revision 1.3.1, a new and important revision to the PMBus standard.

PMBus is a protocol to support the digital management of power supplies. It is an open-standard optimized for simple, standard, flexible, extensible and easy programming. The protocol command language enables communication between the various components of a power system including CPUs, power supplies and power converters.

The seven PMBus industry sessions to be held on Tuesday, March 17th between 8.30am and 11.55am will be aimed at providing potential and existing adopters with insightful and practical information and guidance. They will cover a broad range of topics around the standard including the history and evolution of PMBus and papers on hardware and software considerations. Visitors to APEC 2015 can register to attend the sessions at: http://www.apec-conf.org/registration/. Early registration discounts are available until February 20th.

The current list of PMBus adopters and members includes many major names in the semiconductor market and electronics industry; this underlines the value of PMBus in terms of supporting the design of efficient, optimized power supply products. The current revision - 1.3 with Adaptive Voltage Scaling (AVS) – of the specification includes higher speed communication to reduce latencies, a dedicated AVS bus to statically and dynamically control processor voltages and a range of other enhancements and improvements over version 1.2.

By adopting the SMBus and PMBus standards, companies are better able to develop competitive products with broader market appeal. For more information, visit http://pmbus.org/index.php.

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Contact Notes

SMIF/PMBus

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About The System Management Interface Forum (SMIF)

The System Management Interface Forum (SMIF), Inc., supports the rapid advancement of an efficient and compatible technology base that promotes power management and systems technology implementations. The group's activities include: promoting global development of communications protocols; identification of appropriate applications; providing global educational services; promoting worldwide compatibility and interoperability; and identifying, selecting, augmenting as appropriate, and publishing specifications. The SMIF provides a membership path for any company or individual to be active participants in any or all of the various working groups established by the implementers forums.

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